

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
In re application of:
Asli B. Ucok, et al.
Serial No.: Unknown
Filed: Filed Herewith
For: **MULTI-SUBSTRATE PACKAGE AND METHOD FOR ASSEMBLING
SAME**
Attorney Docket No.: UOM 0319 PUS

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97(b)(1)**

Mail Stop Patent Application
Commissioner for Patents
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Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and § 1.97-1.98, the references listed and identified on the attached Forms PTO/SB08A and/or SB08B are being submitted herewith for consideration by the Examiner. This Statement is being filed in accordance with 37 C.F.R. § 1.97(b)(1).


While this Statement is being filed in compliance with the duty of disclosure, citation of the attached references is not to be construed as an admission that any of the references are "material" as defined under 37 C.F.R. § 1.56(b).

If applicable, a copy of each reference listed on the attached Form PTO/SB08B or any foreign cited art is included herewith. Consideration and entry into the record of these references is respectfully requested.

Respectfully submitted,

Asli B. Ucok, et al.

Date: November 14, 2003

By: 

David R. Syrowik
Reg. No. 27,956
Attorney/Agent for Applicant

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Substitute for Form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known		
			Applicati n Number		
			Filing Date	Filed Herewith	
			First Named Inventor	Asli B. Ucok	
			Group Art Unit		
			Examiner Name		
Sheet	2	of	2	Attorney Docket Number	UOM 0319 PUS
OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
		AL-SARAWI, SAID F., ET AL., A Review of 3-D Packaging Technology, IEE Transactions On Components, Packaging And Manufacturing Technology, 21, No. 1, pp. 2-14, February 1998			
		SCHUENEMANN, MATTHIAS, ET AL., MEMS Modular Packaging And Interfaces, Proc. Of The 50 th Electronic Components And Technology Conference, pp. 681-688, 2000			
		MILLER, DAVID C., ET AL., Microrelay Packaging Technology Using Flip-Chip Assembly, Proc. Of The 13 th International Conference On Micro-Electro-Mechanical Systems, pp. 265-270, Miyazaki, Japan, January 23-27, 2000			
		MASON, ANDREW, ET AL., A Generic Multi-Element Microsystem For Portable Wireless Applications, Proc. Of The IEEE, pp. 1733-1746, August, 1998			
		GOLDSTEIN, HARRY, Packages Go Vertical, IEEE Spectrum, pp. 46-51, August, 2001			
		GANN, KEITH D., Neo-Stacking Technology, HDI Magazine, December, 1999			
		BAKIR, MUHANNAD S., ET AL., Sea of Leads (SOL) Ultrahigh Density Wafer-Level Chip Input/Output Interconnections For Gigascale Integration (GSI), IEEE Transactions On Electron Devices, Vol. 50, No. 10, October, 2003			
		MURARI, BRUNO, Bridging The Gap Between The Digital And Real Worlds: The Expanding Rule of Analog Interface Technologies, 2003 IEEE International Solid-State Circuits Conference, Plenary Session 1.3, February 10, 2003			

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				Application Number	
				Filing Date	Filed Herewith
				First Named Inventor	Asli B. Uçok, et al.
				Group Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	UOM 319 PUS

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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